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Fuji Electric Systems Co., Ltd.  
Controller Div.  
Automation & Solution Business  
Headquarters

## **MICREX-SX Series** **Notice of Functional Expansion of Board Controller** **(SPB Version)**

We would like to thank you for your continued patronage of Fuji Programmable Controllers.  
Please be informed that Fuji Electric Systems has expanded a function in the MICREX-SX-series Board Controller (SPB version) with a firmware upgrade.

### **1. Contents of Change**

| Item            | Applicable model                                    | Expanded function  |
|-----------------|---|--|
| Firmware change | CPU board<br>NW3P08-41C<br>NW3P16-42C<br>NW3P32-42C | (1) Support for task settings that update the current values of high-speed counters. |

### **2. Notes Accompanying Change**

- (1) The change has been applied to products manufactured since November 2008.
- (2) Applicable versions  
The expanded function is supported by firmware version V06 or higher.
- (3) The prices and model numbers remain unchanged.
- (4) The version the SX-Programmer Programming Support Tool must be updated to use the expanded function.
  - Expert (D300win) V3 (NP4H-SEDBV3): V3.4.5.0

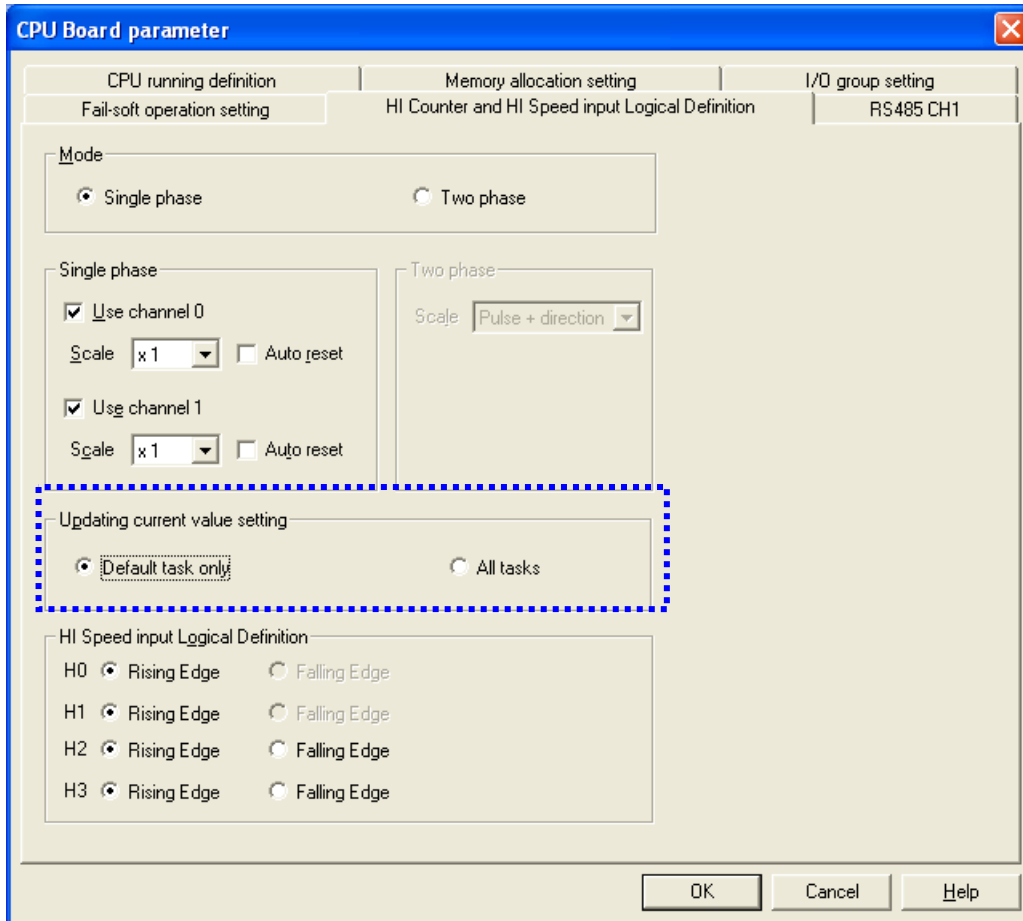
### 3. Expanded Function

#### (1) Overview

With the new version, it is possible to update the current value of a high-speed counter in synchronization with a scheduled task or event task.

Conventionally, the current value of a high-speed counter was updated in synchronization with the execution of the default tasks, which involved a maximum execution time delay equivalent to the execution cycle of the default tasks.

With this expansion, the current value can be updated in synchronization with the execution of an individual task. Therefore, the execution time delay of the current value read in each task will be limited to a maximum period of the task execution time.



The updating of current value setting is by default set to *Default task only* to maintain compatibility with the previous version of the Board Controller.

#### (2) Notes

If the current value of a high-speed counter is read twice or more within a single task, the value that is read may not be consistent as the result of the execution of upstream tasks.